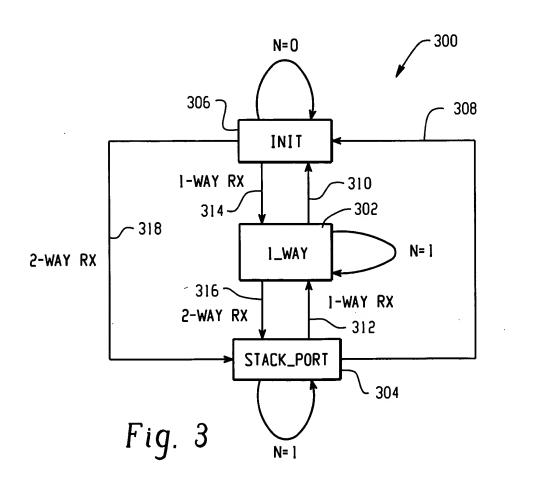


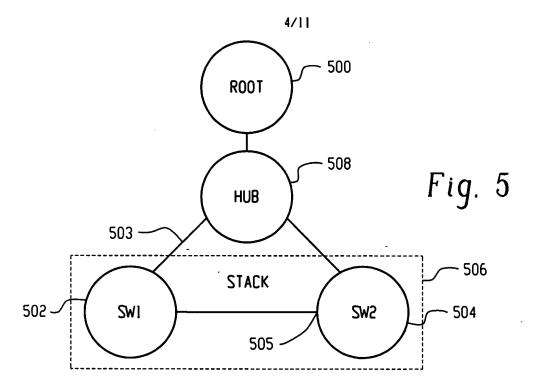
Fig. 2



4 ~ 402		Z 412	414	416	7 418	} 422	420ء	424		
408 406 404	7.5.7.7 3.1.7.7.7.7.7.7.7.7.7.7.7.7.7.7.7.7.7.7.	JGG ING ONLY)	8	ORIGINATOR BRIDGE 104	UKILINAIUK BKIUUL IUB PORT IO	-0E -00X -00X	10	D 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ACKET 7410	LSA_seq_num	HEAD ROUTER SID (FOR DEBUGGING ONLY	NEIGHBOR NUMBER		URIGINATUR BRIUGE IU/ URIGINA NEIGHBOR_1 PORT IO	RESERVED	NEIGHBOR_2 PORT	RESERVED	•	
400 LSA PACKET	RESERVEO	MASTER SID (FOR DEBUGGING ONLY)	ORIGINATOR SWITCH 10		BRIDGE 1US URIGINATUR BRIDGE 1UB NEIGHBOR_1 SWITCH IO	ORIGINATOR PORT IDI	NEIGHBOR_2 SWITCH ID	ORIGINATOR PORT ID2	• • • • • • • • • • • • • • • • • • • •	
		436~MASTEI	434~	416 ORIGINATOR BRIDGE 101	426 NEIGHBOR - 1	430~	428~	432~		

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rig. 4



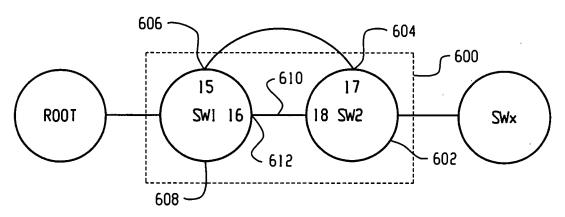


Fig. 6

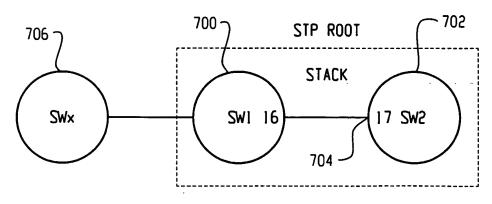


Fig. 7

- 800

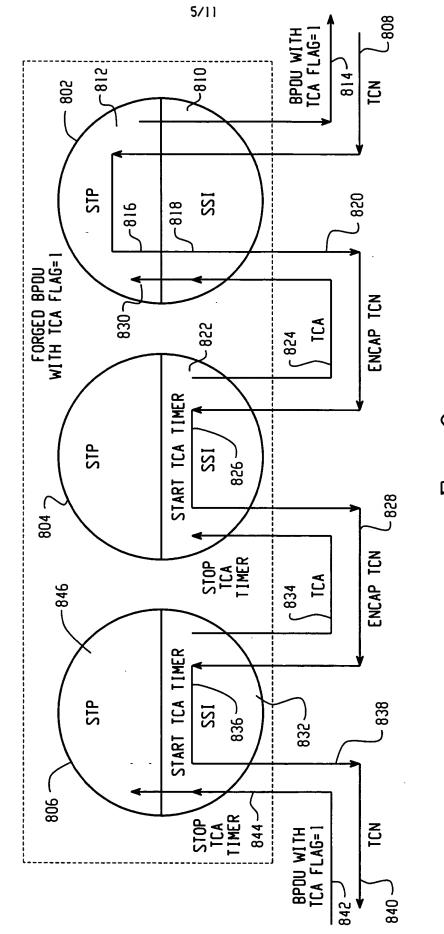
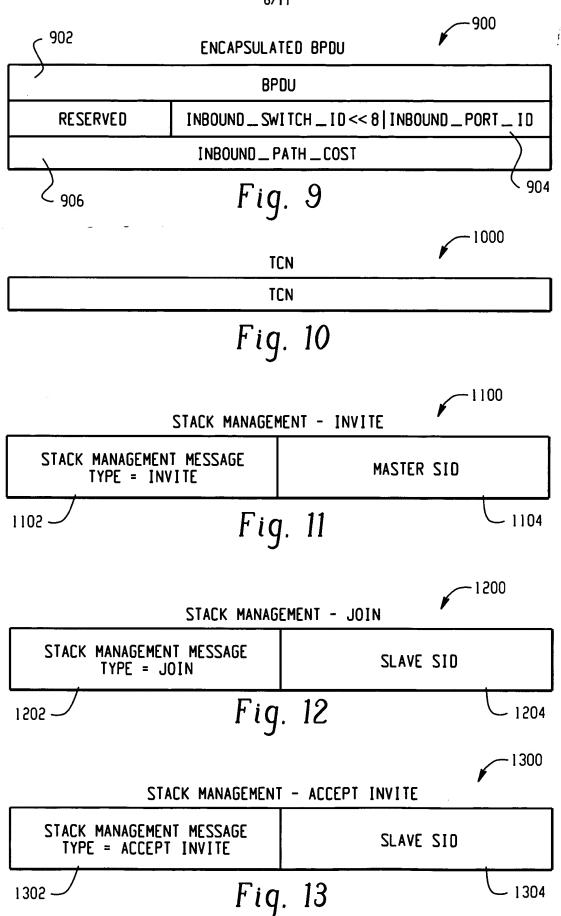


Fig. 8



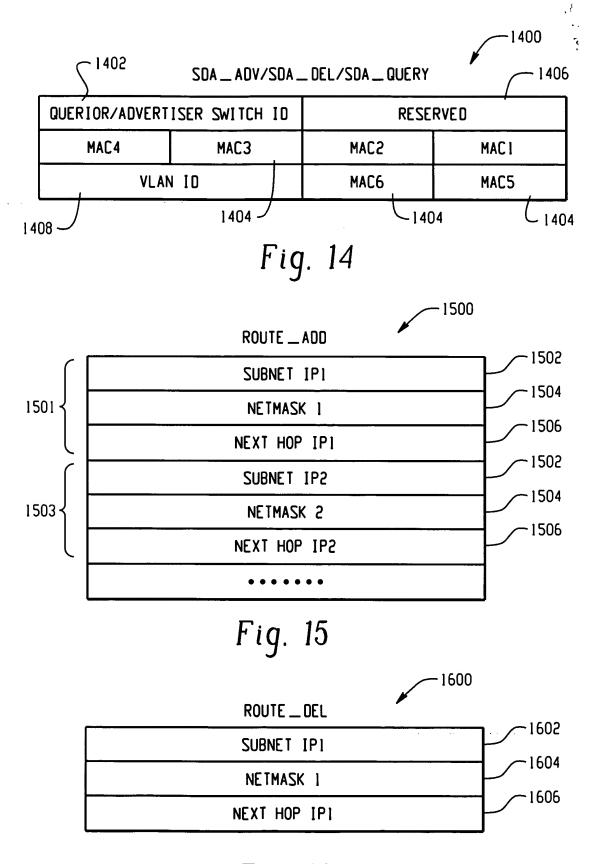
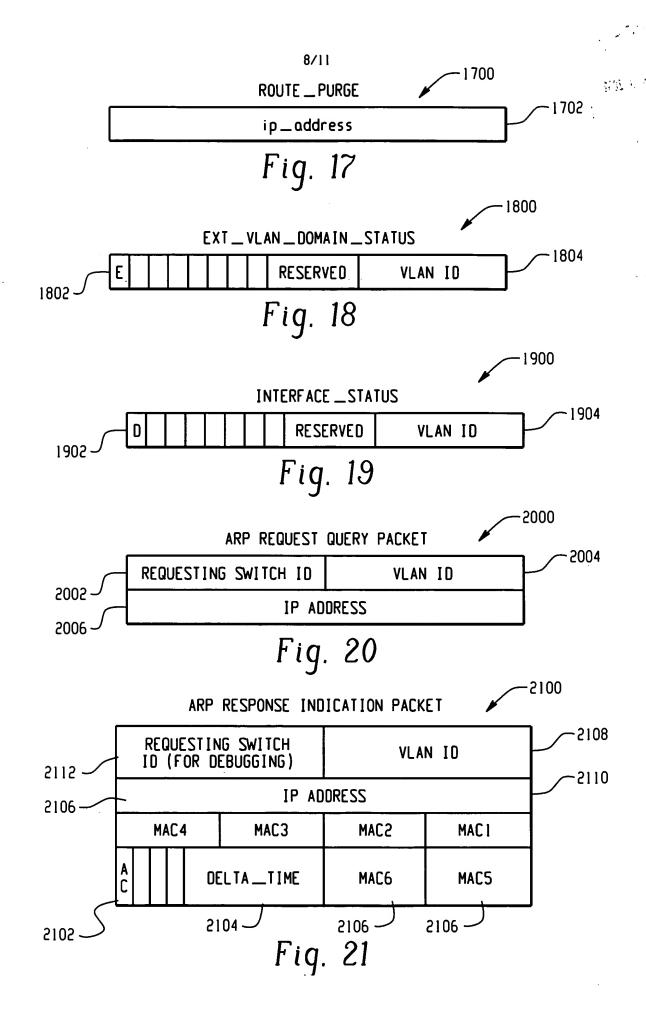


Fig. 16



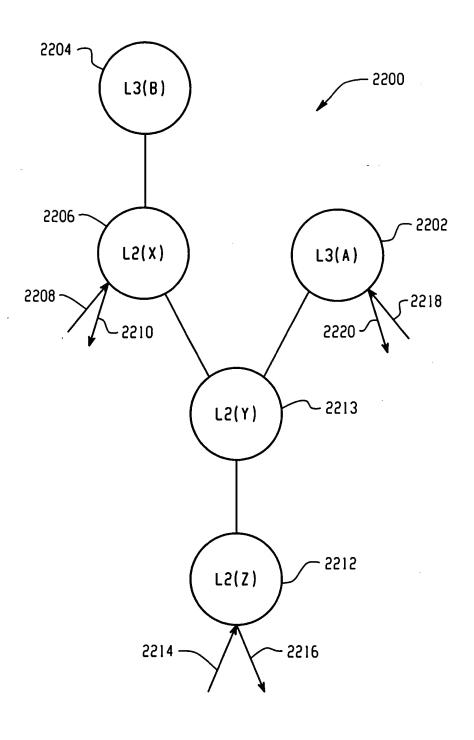


Fig. 22

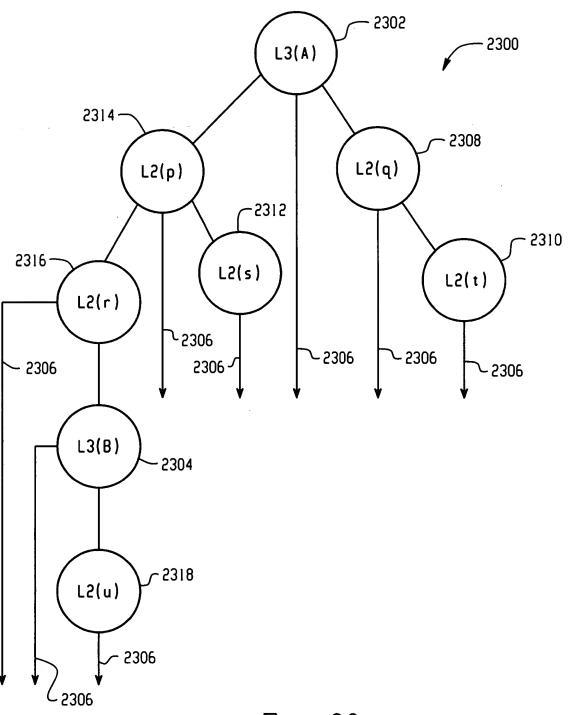


Fig. 23

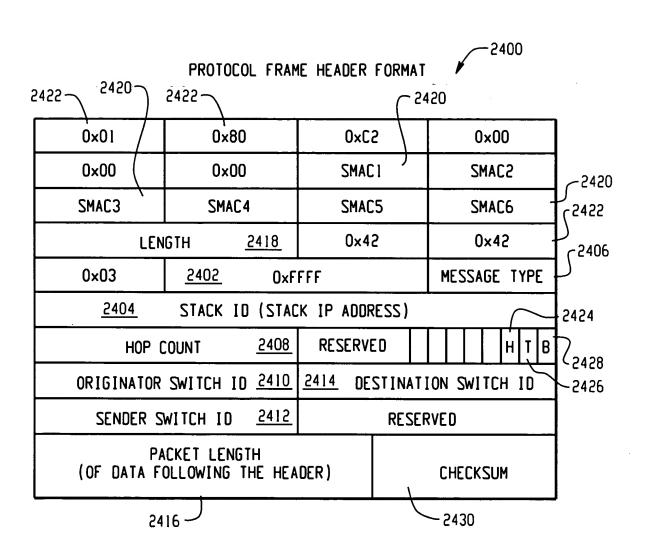


Fig. 24